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Date: August 1, 2005

  
Stormi R. Davis

PATENT APPLICATION  
Attorney's Doc. No.: 2522-026  
Client Ref. No.: AW8116US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ji-Young KIM and Hyoung-Sub KIM

Serial No.: 10/649,262 Examiner: Lindsay, Jr., Walter Lee

Filed: August 26, 2003 Art Unit: 2812

Confirmation No. 7198

For: INTEGRATION METHOD OF A SEMICONDUCTOR DEVICE  
HAVING A RECESSED GATE ELECTRODETRANSMITTAL LETTER

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- ☒ Publication (\$300) and Issue (\$1400.00 large) fees
- ☒ In connection with issuance of a patent:
  - ☐ Supplemental Declaration ☒ PTO Form 85B
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,  
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**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR  
ALLOWANCE**

The Examiner's statement of reasons for Allowance lists some but not all, of the reasons for allowance. The Examiner recited portions of a few of the allowed independent claims and stated that the prior art, either singly or in combination, fails to anticipate or render obvious the limitations of the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

Customer No. 20575

Respectfully submitted,

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